



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Masakatsu ENDO et al.

Application No. 10/506,453

Filed: June 7, 2005

For: IMAGE OUTPUT SYSTEM HAVING
MULTIPLE DEVICES COMMUNICATIONCONNECTED IN MASTER-SLAVE
RELATION

Examiner: Not Assigned

Group Art Unit: 2612

Atty. Docket No. NGBCP007

Date: January 28, 2006

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 28, 2006.

Signed:

Diane Schwanbeck

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Post Office Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of the non-U.S. patent publications of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to consider these references and to acknowledge such consideration by initialing the attached PTO Form 1449 at the appropriate locations.

References L-Q, L-2 – Q-2, O-3 – T-3, and L-4 – P-4 are in the Japanese language. The relevance of each of these references (except for R-3 – T3) may be ascertained by reviewing the corresponding English language abstract (see R-W, R-2 – W-2, U-3 – W-3, and R-4 – V-4, respectively). In addition, machine-generated English translations of References Q, Q-2, and N-4 are attached. The relevance of those references for which no English language abstract is attached may be ascertained by reviewing the attached English translations of the two (2) Notices of Reasons of Rejection and the two (2) Notifications of Reasons for Refusal received in connection with counterpart Japanese applications. In addition, the International Search Report from a counterpart international application is attached for the Examiner's use and information.

Application No. 10/506,453 Information Disclosure Statement

This Information Disclosure Statement (IDS) is being filed before the mailing date of a first Office Action on the merits. Accordingly, no fees are due in connection with the filing of this IDS. In the event it is determined that fees are due, then the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0805 (Order No. NGBCP007).

Respectfully submitted,
MARTINE PENILLA & GENCARELLA, LLP

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	Substitute fo	r form 1449/PTO			Application Number	10/506,453
01	PE				Filing Date:	June 7, 2005
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FFR	2 2006	Statement B	y Applican	it	Art Unit:	2612
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	L	JP 06-029973	02/1994	Kawahara et al.			х
	М	JP 06-125590	05/1994	Takeuchi et al.			х
	N	JP 09-023239	01/1997	Hishikari et al.			х
	0	JP 09-190295	07/1997	Honma			X
	Р	JP 09-219712	08/1997	Saito			х
	Q	JP 10-226139	08/1998	Inoue et al.		х	

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	R	Abstract of Japanese Patent Publication No. 06-029973,				
		Pub. Date: 04 February 1994, Patent Abstracts of Japan				
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		Pub. Date: 06 May 1994, Patent Abstracts of Japan				
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		Pub. Date: 25 August 1998, Patent Abstracts of Japan				
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	Information	Disclosure		First Named Inventor:	Masakatsu ENDO	
	Statement E	By Applicant		Art Unit:	2612	
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	L-2	JP 10-228355	08/1998	Hori			х
	M-2	JP 11-296316	10/1999	Ejiri	٠,		Х
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	0-2	JP 11-327855	11/1999	Oyamada			х
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	Statement E	By Applicant	<u>}</u>	Art Unit:	2612
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R-3	Akisada WATANABE, Basic Knowledge of USB, Transistor Technology, Japan,				
	CQ Press, June 2000, Vol. 37, No. 6, pp. 180-189				
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				Filing Date:	June 7, 2005	
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				Art Unit:		
				Examiner Name:		
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	Q-4						

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